

FREQUENCY SYNTHESIZER FOR DUAL MODE RECEIVER

ABSTRACT OF THE DISCLOSURE

In a portable dual mode receiver circuit, a dual modulus clock system is provided which can, by selective use of integer division on a multiplied master clock, select specific channels with two different channel spacings in several different bands, providing power and space savings and achieving simplicity of operation.

Fig. 1

PA 3176441 v1KRA:dim2001-11-26